

**INDRAPRASTHA INSTITUTE OF INFORMATION TECHNOLOGY DELHI**

(A State University established by Govt. of NCT of Delhi)

**Date: 06/06/2015**

**Subject: Applications are invited for Research Assistant position at IIIT Delhi**

**Closing date of Application- Aug 31, 2015**

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| <b>Post</b>   | Research Assistant  |
| <b>Monthly Remunerations</b>                            | <b>Rs. 25,000/- (all inclusive)</b>   |
| <b>Essential Qualifications</b>                         | <ul style="list-style-type: none"><li>• ME/MTech in ECE with good MATLAB, VHDL and FPGA knowledge</li><li>• Exceptional BE/BTech students may be considered</li><li>• Proficient in digital signal processing, FPGA implementation various algorithms</li><li>• Good interpersonal and communication skills (both oral and written)</li></ul> |
| <b>Desirable Qualifications and Experience Duration</b> | <ul style="list-style-type: none"><li>• Project work on VHDL and FPGA implementation, partial reconfiguration</li><li>• Experience in working with companies/universities, in a research domain</li><li>• Good IEEE publications and/ or system demonstrations using FPGA</li></ul>   |
| <b>Duration</b>   | 6 months (Extension or recommendation for PhD program for good candidates as per IIITD admission policy)  |

Please send your CV by Aug 31, 2015 via e-mail to –[sumit@iiitd.ac.in](mailto:sumit@iiitd.ac.in).

Please mention “Application for RA” in the subject line of your email.

For more information: [www.iiitd.edu.in/~sumit/](http://www.iiitd.edu.in/~sumit/)

Regards,  
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